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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

1450, Alexandria, VA 22313-1450.

Patent Application

Applicant(s): D. Bhattacharya et al.

Case:

3-5-8-1-25

Serial No.:

10/768,771

Filing Date: Group:

January 30, 2004 ·To Be Assigned

Examiner:

To Be Assigned

Title:

Improved Semiconductor Resistor

TRANSMITTAL OF FORMAL DRAWINGS

Mail Stop PGPUB Drawings Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Attention: Official Draftsperson

Date: March 29, 2004

Sir:

Applicants submit herewith five (5) sheets of formal drawings in the above-referenced application.

Respectfully submitted,

Wayne L. Ellenbogen

Reg. No. 43,602

Ryan, Mason & Lewis, LLP Attorney for Applicant(s)

90 Forest Avenue

Locust Valley, NY 11560

(516) 759-7662